

AMENDMENTS TO THE CLAIMS

(IN FORMAT COMPLIANT WITH THE REVISED 37 CFR 1.121)

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1. (CURRENTLY AMENDED) An apparatus for coupling a peripheral device to a host comprising:

an interface circuit configured to receive a request from said host and present a response to said request to said host; and

5 a logic circuit coupled to said interface circuit and configured to (i) automatically generate said response when said request is serviceable by said ~~apparatus~~ or logic circuit and (ii) pass (a) said request from said interface circuit to an external circuit and (b) said response from said external circuit to said  
10 interface circuit when said request is not serviceable by said ~~apparatus~~ logic circuit.

2. (CURRENTLY AMENDED) The apparatus according to claim 1, wherein said interface circuit ~~comprises~~ and said logic circuit are part of a serial interface engine (SIE).

3. (ORIGINAL) The apparatus according to claim 1, wherein said external circuit comprises a processor.

4. (ORIGINAL) The apparatus according to claim 3, wherein said external circuit comprises a processor selected from

the group consisting of a digital signal processor (DSP), a microprocessor, and an application specific integrated circuit (ASIC).

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5. (ORIGINAL) The apparatus according to claim 1, wherein said apparatus comprises a universal serial bus (USB) peripheral device.

6. (ORIGINAL) The apparatus according to claim 1, wherein said logic circuit is configured to generate said response to said request using information received from said external circuit.

7. (CURRENTLY AMENDED) The apparatus according to claim 6, further comprising a memory configured to store said information, wherein said information comprises a descriptor table.

8. (CURRENTLY AMENDED) The apparatus according to claim 1, wherein said logic circuit is configured to automatically service an enumeration request.

9. (ORIGINAL) The apparatus according to claim 1, wherein said request comprises a request as defined in Chapter 9 of the Universal Serial Bus (USB) Specification, revision 2.0.

10. (ORIGINAL) The apparatus according to claim 1, wherein said logic circuit is configured to pass to said external circuit a request selected from the group consisting of a class request, a vendor request, a custom driver request, and requests  
5 implemented to support USB specification changes and enhancements.

11. (CURRENTLY AMENDED) The apparatus according to claim 1, wherein ~~said response from said external circuit comprises~~ is configured to generate a stall signal as said response when said request is not recognized by said external circuit.

12. (CURRENTLY AMENDED) An apparatus comprising:  
means for receiving a request from a host and presenting a response to said request to said host; and

means for ~~(i)~~ generating said response automatically when  
5 said request is serviceable by said ~~apparatus or (ii)~~ generating means; and

means for passing (i) said request from said receiving and presenting means to an external circuit and (ii) said response from said external circuit to said receiving and presenting means  
10 when said request is not serviceable by said ~~apparatus~~ generating means.

13. (CURRENTLY AMENDED) A method for interfacing a peripheral device to a host comprising the steps of:

receiving a request from said host;

~~directly~~ automatically responding to said request when  
5 said request is recognized;

when said request is not recognized, passing said request to an external circuit;

receiving a response from said external circuit; and

passing on said response to said host.

14. (CURRENTLY AMENDED) The method according to claim 13, wherein further comprising the step of:

generating said response from as a stall command when  
said request is not recognized by said external processor comprises  
5 a stall command circuit.

15. (ORIGINAL) The method according to claim 13, wherein said external circuit comprises a processor selected from the group consisting of a digital signal processor (DSP), a microprocessor, and an application specific integrated circuit (ASIC).

16. (ORIGINAL) The method according to claim 13, wherein said requests are received via a serial bus in accordance with the

Universal Serial Bus (USB) Specification, revision 1.0, 1.1, or 2.0.

17. (CURRENTLY AMENDED) The method according to claim 13, wherein said ~~USB~~ request comprises an enumeration request.

18. (ORIGINAL) The method according to claim 13, wherein said request presented to said external circuit comprises a request selected from the group consisting of a class request, a vendor request, a custom driver request, and a request implemented to support changes and/or enhancements to a communication protocol.

19. (CURRENTLY AMENDED) The method according to claim ~~13~~ 17, wherein the step of automatically responding to said response from request comprises enumerating said peripheral device without passing said enumeration request to said external circuit ~~comprises a stall response.~~

20. (ORIGINAL) The method according to claim 13, further comprising the step of receiving one or more descriptor tables from said external circuit.